



Student for IC Digital Design & Verification



In this role you will be able to:

- › **Specify, design, implement, simulate and verify digital blocks;**
- › Perform **RTL design and synthesis, gate level simulation, power and static timing analysis;**
- › **Build top/block level verification environments** consisting in test benches that check proper behavior of circuits;
- › **Create verification plans and test suites** based on design specification;
- › **Make regression simulation and reporting** using dedicated software tools and analysis of coverage and performance.

You are best equipped for this role if you:

- › Are **studying Electronics Engineering or Computer Science;**
- › Have **knowledge of simulation tools**, such as
 - › Modelsim, Ncsim or similar digital simulators for design
 - › Specman, Xcelium/Ncsim, Vmanager or similar for verification;
- › Have **programming skills** in
 - › VHDL, Verilog and C/C++
 - › Python, Perl or TCL for scripting
 - › SystemVerilog or E for verification.

<https://www.infineon.com/cms/en/careers/>

