



LAYOUT ENGINEER

About On Semiconductor

ON Semiconductor (Nasdaq: ON) is driving energy efficient innovations, empowering customers to reduce global energy use. The company is a leading supplier of semiconductor-based solutions, offering a comprehensive portfolio of energy efficient power management, analog, sensors, logic, timing, connectivity, discrete, SoC and custom devices. The company's products help engineers solve their unique design challenges in automotive, communications, computing, consumer, industrial, medical, aerospace and defense applications. ON Semiconductor operates a responsive, reliable, world-class supply chain and quality program, a robust compliance and ethics program, and a network of manufacturing facilities, sales offices and design centers in key markets throughout North America, Europe and the Asia Pacific regions.

Job Description:

Core responsibilities include the following:

- Individual contributor in the design team to create innovative layout.
- Timely execution of development efforts by way of robust layout design.
- Ability to estimate layout schedule for a given circuit, layout planning and provide early feedback to circuit design engineers etc.
- Works on problems of moderate scope where analysis of situations or data requires a review of identifiable factors.
- Develop and maintain the skillset and system knowledge relevant to the product line.
- Communicate with design engineers to negotiate any necessary layout trade-offs as needed to build complex analog layout.

Requirements:

- Bachelor or Master Degree in Electrical Engineering.
- Familiarity with analog matching requirements and practices
- Expertise on Cadence virtuoso custom/analog layout platform would be a plus
- Knowledge of layout techniques for device matching and minimized parasitic
- Communicate with design engineers to negotiate any necessary layout trade-offs as needed to build complex analog layout.
- Ability to work with circuit designers that are located remotely. As a result, solid documentation and communication skills are required.
- Familiar with the following tools: Cadence, Mentor and Synopsis tool suites including schematic capture, Hspice/Spectre and HspiceRF/SpectreRF, Nanosim or XA, Verilog-A/AMS, LVS/DRC verification. Shell scripting and programming experience are highly desirable.
- Excellent written and oral English communication skills and ability to work with global teams.

If you are interested in this position, please send us the CV to onjob-romania@onsemi.com.

More info: www.onsemi.com.